FORMAL SEMANTICS FOR INTERACTING UML SUBSYSTEMS

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Abstract
So far most work on formal semantics for the Unified Modeling Language (UML) has concentrated on single diagrams. To provide a formal foundation for complete system specifications, one needs to put models for the different diagrams into context. We provide a formal semantics for UML subsystems that incorporates a formal semantics of the diagrams contained in a subsystem. It provides message-passing between objects or components specified in different diagrams, including a dispatching mechanism for events, and the handling of actions. It enables one to compose subsystems from sets of subsystems and allows them to interact by passing messages.

We give consistency conditions for the diagrams in a subsystem and define a notion of behavioural equivalence and two kinds of refinement for UML subsystems.

Keywords: UML, formal semantics, refinement, Abstract State Machines.

1. Introduction

The Unified Modeling Language (UML) [RJB99] is the de-facto industry standard for specifying object-oriented software systems (for an introduction cf. [SP00]). Even though rather precisely defined compared with other modeling languages, its semantics is given only in prose form [UML01], leaving room for ambiguities (a problem especially when providing tool support or trying to establish behavioural properties of UML specifications). Thus we need a mathematically precise semantics for UML.

There has been a substantial amount of work towards providing a formal semantics for UML diagrams (including [EFLR99; BCR00; Ste01]).

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However, most work only provides models for single UML diagrams in isolation. When trying to give a precise mathematical meaning to whole UML specifications, one needs to be able to combine the formal models for the different kinds of diagrams.

**Joint formal execution.** In this paper we describe some results on how to formally model UML diagrams in context. We provide a formal semantics for UML subsystems that incorporates the formal semantics of the diagrams contained in the subsystem (here we consider statechart and activity diagrams; a treatment of the remaining diagrams is given in [Jür02]). Specifically, our semantics

- models actions and internal activities explicitly (rather than treating them as atomic given events), in particular the operations and their parameters employed in them,
- provides message-passing between objects or components specified in different diagrams, including a dispatching mechanism for events and the handling of actions, and thus
- allows whole specification documents to be based on a formal foundation.

In particular, we show how to compose subsystems by including them into other subsystems.

Thus it prepares the ground for further work that may

- provide tool-support based on this precise semantics, in particular allowing complete specifications to be simulated, and
- provide the possibility of complete executable UML specifications.

**Statechart, activity diagrams, subsystems.** The semantics for statecharts presented here is based on [BCR00], which however had to be extended significantly to incorporate the features mentioned above. Our formal semantics for subsystems and their interactions seems to be the first given in the published literature.

**Consistency.** Furthermore we give some conditions for consistency, relating different diagram kinds in a UML system specification, which is an issue that seems to attract increasing attention.

**Equivalence.** We define a notion of behavioural equivalence between UML subsystems. This can be used e.g. to verify consistency of two subsystem specifications that are supposed to describe the same behaviour,
Refinement. In UML, refinement denotes a certain kind of dependency relation between model elements [UML01, 2-18]. There is no constraint on the semantic relationship between the model elements (also in the heuristics for state machine refinement at [UML01, 2-177]).

When trying to establish system properties, behavioural conformance of refinement can lead to great savings (properties may be easier to establish at a more abstract level; preservation by refinement means that this is in fact sufficient). We define a kind of refinement (inspired by [BS01]) that provides full behavioural conformance.

UML for security. One of the motivations for this work is to provide a foundation for using UML for developing security-critical systems [Jür01b], where a mathematically precise modelling is indispensable. The semantics presented here has been extended to include possible adversary behaviour to reason about security requirements [Jür02]. In particular, the preservation results in [Jür01a] extend to the refinement presented here.

For readability, we consider simplified UML diagrams. Our approach works just as well without the simplifications. More details can be found in [Jür02].

Outline. In Section 2 we give basic definitions of Abstract State Machines needed for our semantics. We provide a sketch of the semantics of statechart diagrams and activity diagrams in Section 3, in order to be able to show how these fit together in the context of UML subsystems, the semantics of which is presented in Section 3.3. We define two kinds of refinement for UML subsystems in Section 4 and give an example in Section 5. We end with pointers to related work, a conclusion and indication of future work.

2. Abstract State Machines

ASMs [Gur95] provide a rather flexible framework for formal modelling. They have been used e.g. to give a formal semantics for a non-trivial part of Java [SSB01].

We collect some central concepts. A vocabulary is a finite collection of function names, each of a fixed arity. We assume a set of variables. A state $A$ of vocabulary $\text{Voc}(A)$ is a non-empty set $X$ containing distinct elements $true$, $false$, and $\text{undef}$ together with interpretations of
the function names in \( \text{Voc}(A) \) on \( X \). Relations and sets are viewed as functions taking values in \( \{ \text{true}, \text{false} \} \). An ASM is executed by updating its state (e.g., changing the interpretations of the names in the vocabulary) iteratively by applying update rules some of which are given in the following.

**Update** The update rule \( f(\bar{s}) := t \) of the ASM \( A \) updates \( f \) at the tuple \( \bar{s} \) to map to the element \( t \).

**Conditional** For a Boolean term \( g \) rules \( R, S \), the rule if \( g \) then \( R \) else \( S \) executes \( R \) if \( g \) holds, otherwise \( S \).

**Blocks** The rule do – in – parallel \( R_1, \ldots, R_k \) enddo executes the rules \( R_1, \ldots, R_k \) simultaneously if they are mutually consistent: for any two update rules \( f(\bar{s}) := t \) and \( f(\bar{s}) := t' \), we have \( t = t' \). Otherwise, execution stops.

**Sequential Composition** For rules \( R, S \), the rule seq \( R, S \) endseq executes \( R \) and \( S \) sequentially.

**Do–forall** For a variable \( v \), a Boolean term \( g(v) \), and a rule \( R(v) \), the rule forall \( v \) with \( g(v) \) do \( R(v) \) executes \( R(a) \) for all \( a \) such that \( g(a) \) holds, if they are mutually consistent. Otherwise, execution stops.

**Loop through list** For a variable \( v \), a list \( X \), and a rule \( R(x) \), the rule loop \( v \) through list \( X \) \( R(v) \) executes \( R(x) \) iteratively for all \( x \in X \).

An abstract state machine consists of a set of states and an update rule. It is executed by iteratively firing the update rule.

For multi-sets, we write \{ \} instead of the usual brackets. For two multi-sets \( M \) and \( N \), \( M \uplus N \) denotes their union and \( M \setminus N \) the subtraction of \( N \) from \( M \). For a multi-set \( M \) and a set \( X \), we write \( M \setminus X \) for the multi-set of those elements in \( M \) (preserving their cardinalities) that are also elements of \( X \).

3. **Formal Semantics for a fragment of UML**

The set \( \text{MsgNm} \) of message names consists of finite sequences of names \( n_1, n_2, \ldots, n_k \) where \( n_1, \ldots, n_{k-2} \) are names of UML subsystems (to be defined below), \( n_{k-1} \) is a name of an object, and \( n_k \) is the local name of the message. The idea is that a message \( n_1, n_2, \ldots, n_k \) will be delivered as the message with name \( n_k \) to the object with name \( n_{k-1} \) which is part of the (iteratively nested) sequence of subsystems \( n_{k-2}, \ldots, n_1 \). Messages in \( \text{MsgNm} \) can be operations, signals, and return messages. For each operation \( op \) there is a corresponding return signal \( \text{return}(op) \), assumed to be given explicitly. We write \( \text{Events} \) for
the set of events of the form \( op(\exp_1, \ldots, \exp_n) \) with \( op \in \text{MsgNm} \) and \( \exp_1, \ldots, \exp_n \in \text{Exp} \) (for a set \( \text{Exp} \) of expressions which includes a set \( \text{Var} \subseteq \text{Exp} \) of variables). We define \( \text{msgname}(m) \overset{\text{def}}{=} msg \) to be the name of the message \( m = \text{msg}(\exp_1, \ldots, \exp_n) \). In our model, every object \( O \) has associated multi-sets \( \text{inQueue}(O) \) and \( \text{outQueue}(O) \). We model sending a message \( msg \) from an object \( S \) to an object \( R \) as follows:

1. The object \( S \) puts \( R.msg \) into its multi-set \( \text{outQueue}(S) \).
2. A scheduler distributes the messages from out-queues to the intended in-queues (removing the head); in particular, \( R.msg \) is removed from \( \text{outQueue}(S) \) and \( msg \) added to \( \text{inQueue}(R) \).
3. The object \( R \) removes \( msg \) from its in-queue and processes its content.

In the case of operation calls, we also need to keep track of the sender to allow sending return signals. Modelling communication is thus very flexible; we can adjust the scheduler according to the underlying communication layer (and take account e.g. of security [Jür01b] or performance issues).

In the following sections, we define the abstract syntax of various UML diagrams using mathematical notation, and then give a precise semantics of the modelled system behaviour using ASMs. For space reasons, we can only present simplified versions of the diagrams; more details are in [Jür02].

3.1. Statechart Diagrams

Abstract syntax. A statechart diagram \( D = (\text{Obj}_D, \text{Cls}_D, \text{Sts}_D, \text{ln}_D, \text{Trs}_D) \) is given by an object name \( \text{Obj}_D \), a class name \( \text{Cls}_D \), a set of states \( \text{Sts}_D \), an initial state \( \text{ln}_D \), and a set of transitions \( \text{Trs}_D \). \( \text{Sts}_D \) is a set of tuples \( S = (\text{nm}(S), \text{ety}(S), \text{init}(S), \text{sta}(S), \text{int}(S), \text{ext}(S)) \) where

- \( \text{nm}(S) \) is a string of characters called the name of the state,
- \( \text{ety}(S) \in \text{Action} \) is called the entry action,
- \( \text{init}(S) \in \text{Sts}_D \cup \{ \text{undef} \} \) is the initial substate of \( S \),
- \( \text{sta}(S) \subseteq \text{Sts}_D \) is the set of substates of \( S \),
- \( \text{int}(S) \) is called the internal activity (or do-activity), and
- \( \text{ext}(S) \in \text{Action} \) is the exit action.

Here we write \( \text{Action} \) for the set of actions of the following forms:

- \( \text{Call/send action:} \) \( \text{call}(\text{msg}(a_1, \ldots, a_n)) \) resp. \( \text{send}(\text{msg}(a_1, \ldots, a_n)) \) for \( \text{msg} \in \text{MsgNm} \) and \( a_i \in \text{Exp} \).
- \( \text{Assignment:} \) \( \text{att := exp} \) for an attribute \( \text{att} \) and \( \text{exp} \in \text{Exp} \).

5
We assume that for every internal activity $actv$ there is an ASM rule $ActvRule(actv)$.

$Trs_D$ is a set of tuples $t = (\text{src}(t), \text{evt}(t), \text{grd}(t), \text{act}(t), \text{tgt}(t))$ where

- $\text{src}(t) \in Sts_D$ is the source state of $t$,
- $\text{evt}(t) \in Events$ is the triggering event of $t$,
- $\text{grd}(t)$ is a Boolean expression called the guard of $t$,
- $\text{act}(t) \in \text{Action}$ is an action (to be performed when firing $t$),
- $\text{tgt}(t) \in Sts_D$ is the target state of $t$.

$\text{evt}(t)$ must be of the form $op(exp_1, \ldots, exp_n)$ with mutually distinct $exp_1, \ldots, exp_n \in \text{Var}$. We assume a special event $\text{ComplEv} \in Events$ (without parameters) as in [BCR00]. If $\text{intern}(t) = \text{true}$ then $t$ is called an internal transition, otherwise it is called external.

**Behavioural semantics.** We give a formal semantics of statechart diagrams using ASMs. It is based on [BCR00], which however had to be extended significantly to incorporate explicit modelling of actions and internal activities and message-passing between objects or components in different diagrams. We fix a statechart diagram $D$ modelling an object $O \overset{\text{def}}{=} \text{Obj}_D$ and give its behavioural semantics as an ASM $[D]^{SC}$.

The vocabulary of $[D]^{SC}$ consists of the following names:

- the set name $\text{currState}$ (storing the set of currently active states),
- the multi-set names $\text{inQueue}(O), \text{outQueue}(O)$ (the input resp. output queue),
- the function name $\text{trigsys}()$ mapping each operation name to the object or subsystem that last sent it (to allow sending back return values),
- the function name $\text{finished}$ (mapping states to Boolean values, indicating whether a given state is finished), and
- all variables names in $\text{evt}(t)$ for all $t \in Trs_D$.

The Boolean $\text{finished}_t$ is set to $\text{true}$ at the end of an internal activity $ActvRule(\text{int}(S))$.

The ASM $[D]^{SC}$ has two rules, $\text{Initialize}(D)$ and $\text{Main}(D)$, given below (defined using other rules omitted for space restrictions; they are adapted from [BCR00] and can be found in [Jär02]). The former rule initializes the variables of the ASM. The latter rule consists of selecting the event to be executed next (where priority is given to the completion event) and executing it, and then executing the rules for the internal activities in a random order.
Rule Initialize($D$)
do – in – parallel
   inQueue($Obj_D$) := ∅  outQueue($Obj_D$) := ∅
   currState := {ini,$D$}  finishedini,$D$ := false
endo
Rule Main($D$)
seq if Completed $\neq$ ∅ then eventExecution(ComplEv)
   else choose $e$ with $e \in$ inQueue($O$) do
      seq inQueue($O$) := inQueue($O$) \ $\{e\}$
      if $e = op_{sender}[args] \in$ Operation
      then seq $e := op[\text{args}]$
         trigSusy($e$) := sender endseq
      eventExecution($e$)
   endseq
loop $S$ through set currState
   seq finished$S$ := false
      ActvRule(int($S$))
Endseq
endseq

Here eventExecution($e$) is a macro whose execution models the execution of the event $e$, which involves firing its associated action as follows.

Rule ActionRule(call($op[\text{args}]$))
   outQueue($O$) := outQueue($O$) \ $\{op,O[\text{args}]\}$
Rule ActionRule(send($e$))
   outQueue($O$) := outQueue($O$) \ $\{e\}$
Rule ActionRule(send(return($op$)$\langle a \rangle$))
   outQueue($O$) := outQueue($O$) \ $\{\text{trigSusy($op$).return($op$)$\langle a \rangle$}\}$

3.2. Activity diagrams

An activity diagram is a special case of a state machine that is used to model processes involving one or more classes [UML01, B-2]. The internal activities in an activity diagram are given as an object or subsystem name $S$. Thus activity diagrams coordinate the execution of objects and subsystems. Note that these can be specified to proceed at different speeds, for example by including delay actions.

3.3. Subsystems and System Specifications

A subsystem $S = (\text{name}(S), \text{Msgs}(S), \text{Ints}(S), \text{Ssd}(S), \text{Ad}(S), \text{Bd}(S))$ is given by

- the name name($S$) of the system,
- a set of accepted messages $\text{Msgs}(S)$,
- a set of interfaces $\text{Ints}(S)$,
- a static structure diagram $\text{Ssd}(S)$ (defined in Section 3.3)
- an activity diagram $\text{Ad}(S)$, and
- a set $\text{Bd}(S)$ of statechart diagrams (each for a different object).

Each statechart specifies the behaviour of one object in the subsystem, so $\text{Bd}(S)$ is empty if there are no classes (but only subsystems) in $\text{Ssd}(S)$.

Subsystems may contain further kinds of diagrams omitted here.

For simplicity, we do not explicitly model creation and deletion of objects, but only activation and inactivation (following [KW01, p. 15]). The run-to-completion step for each object is performed in parallel, subject to the flow of control specified by the activity diagram.

**Class Diagrams or Static structure Diagrams.** A class model $C = (\text{name}, \text{att}, \text{mess}, \text{int})$ is given by

- a name $\text{name}$,
- a set of attribute names $\text{att}$,
- a set of message names $\text{mess}$,
- and a set $\text{int}$ of interfaces of the form $I = (\text{name}, \text{msg})$ where $\text{name}$ is the interface name and $\text{msg}$ a set of message names.

A dependency is a tuple $(\text{dep}, \text{indep}, \text{int}, \text{stereo})$ consisting of

- class or subsystem names $\text{dep}$ and $\text{indep}$,
- an interface name $\text{int}$, and
- a stereotype $\text{stereo} \in \{\text{call}, \text{send}\}$.

Note that the existence of a dependency $(\text{dep}, \text{indep}, \text{int}, \text{stereo})$ with $\text{stereo} \in \{\text{call}, \text{send}\}$ implies that the object $\text{dep}$ knows of the object $\text{indep}$ (otherwise $\text{dep}$ could not send a signal to or call the object $\text{indep}$).

A static structure diagram $D = (\text{SuSys}(D), \text{Dep}(D))$ is given by a set $\text{SuSys}(D)$ consisting of class models or subsystems, and a set $\text{Dep}(D)$ of dependencies $(\text{dep}, \text{indep}, \text{int}, \text{stereo})$. Other modelling elements (such as associations) can be added without complication. A static structure diagram is called a class diagram if it contains no subsystems.

**Consistency between UML diagrams.** A subsystem is called consistent if the following conditions are satisfied:

- The object and subsystem names appearing as activities in the activity diagram are part of the static structure diagram. The behaviour of the objects is defined by statecharts.
- For each call resp. send action in a statechart diagram, the static structure diagram $C$ must have a dependency stereotyped \langle call\rangle resp. \langle send\rangle between the objects (or their interfaces) in question. For each assignment action $att := \text{exp}$ in $S$, $att$ is contained in the set of attributes of $C\text{ls}_S$ given in $D$.
- The operations offered by the subsystem must be offered by class models or subsystems in the static structure diagram.
- For statecharts $S$ and $T$ we have $S = T$ or $\text{Obj}_S \neq \text{Obj}_T$.

**Behavioural semantics of Subsystems.** Suppose we are given a consistent subsystem $S$. The behavioural interpretation of $S$ is given by the ASM $[S]^{SuS_y}$ with the rules \texttt{Initialize}(S) and \texttt{Main}(S) defined in the following.

We write $\text{Acts}(S)$ for the set of object and subsystem names giving the activities in $\text{Ad}(S)$. If $act \in \text{Acts}(S)$ is an object whose behaviour is given by a statechart diagram $D \in \text{Bd}(S)$ (i.e. $\text{Obj}_S = act$), we write $[S]_act^{BD}$ for $[D]^{SC}$, $\texttt{BDInitialize}_S(act)$ for \texttt{Initialize}(D) and $\texttt{BDMain}_S(act)$ for \texttt{Main}(D). If $act \in \text{Acts}(S)$ is a subsystem in $\text{Ssd}(S)$, we write $[S]_act^{BD}$ for $[act]^{SuS_y}$, $\texttt{BDInitialize}_S(act)$ for \texttt{Initialize}(act) and $\texttt{BDMain}_S(act)$ for \texttt{Main}(act).

The vocabulary of $[S]^{SuS_y}$ includes the following names:
- the names in the vocabularys of $[S]^{SuS_y}$ for all $S \in \text{SuSys}(\text{Ssd}(S))$, $[\text{Ad}(S)]^{AD}$ and $[\text{Bd}(S)]^{BD}$ for all $S \in \text{Acts}(S)$,
- the names in $\text{Queue}(S)$ and $\text{outQueue}(S)$ (in and out queue of $S$).

We define the two rules.

\textit{Rule Initialize}(S)
\begin{verbatim}
do - in - parallel
  Initialize(Ad(S))
  forall S with S \in \text{Acts}(S) do
    BDMain_S(S)
    inQueue(S) := \emptyset    outQueue(S) := \emptyset
  enddo
\end{verbatim}

\textit{Rule Main}(S)
\begin{verbatim}
seq
  forall S with S \in \text{Acts}(S) do
    inQueue(S) := inQueue(S) \cup \{(tail(e) : e \in (inQueue(S) \setminus \text{Msgs}(S)) \land \text{head}(e) = S)\}
    inQueue(S) := \emptyset
    Main(Ad(S))
  forall S with S \in \text{Acts}(S) do
\end{verbatim}
\[
\text{inQueue}(S) := \text{inQueue}(S) \uplus \\
\bigcup_{T \in \text{Acts}(S)} \{ \text{tail}(e) : e \in \text{outQueue}(T) \land \text{head}(e) = S \}
\]
\[
\text{outQueue}(S) := \text{outQueue}(S) \uplus \\
\bigcup_{T \in \text{Acts}(S)} \{ \text{tail}(e) : e \in \text{outQueue}(T) \land \text{head}(e) = S \}
\]
\[
\text{forall } S \text{ with } S \in \text{Acts}(S) \text{ do}
\]
\[
\text{outQueue}(S) := \emptyset
\]
endseq

System specifications. A UML system is given by a UML subsystem which is intended to model the complete system under consideration (rather than just a part).

Given a system \( S \) and a sequence \( \vec{I}_1, \ldots, \vec{I}_n \) of multi-sets, the timed behaviour \( [S]^t(\vec{I}_1, \ldots, \vec{I}_n) \) is defined to be the set of possible contents of \( \text{outlist}(S) \) after the execution of any instantiation of the following ASM rule on the ASM \( [S]^{S_u S_y} \).

\text{Rule tSys}_A(S)
seq
\text{outlist}(S) := \emptyset
Initialize(S)
loop i through list \([1 \ldots n]\)
\text{seq}
\text{inQueue}(S) := \text{inQueue}(S) \uplus \vec{I}_i
\text{Main}(S)
\text{outlist}(S) := \text{outlist}(S).\text{outQueue}(S)
\text{outQueue}(S) := \emptyset
endseq
endseq

The untimed behaviour of a system \( S \) is a function \( [S]^u() \) from multi-sets of events (the inputs to the system during its execution) to sets of multi-sets of events (the possible outputs of the system during its (possibly non-terminating) execution). It is defined at the meta-level. Given a multi-set \( I \), we define \( [S]^u(I) \) to be the fix-point of the content of \( \text{outQueue}(S) \) after firing the rule

\text{seq}
\text{Initialize}(S) \text{ inQueue}(S) := I
endseq

and iterating the rule \( \text{Main}(S) \). The fix-point exists, because \( S \) changes \( \text{outQueue}(S) \) only by adding elements: it is the directed union of the contents of \( \text{outQueue}(S) \) after each execution of \( \text{Main}(S) \).

4. Equivalence and Refinement

Definition 1 (Property Refinement) Suppose we are given two subsystem specifications \( S \) and \( S' \) and a set \( \mathcal{M} \) of message names. \( S' \) is a
timed (resp. untimed) $\mathcal{M}$-refinement of $S$ if conditions I and IIA (resp. conditions I and IIb) are fulfilled:

I $\text{Msgs}(S) \cap \mathcal{M} \subseteq \text{Msgs}(S') \cap \mathcal{M}$

IIA for all sequences $I_1, \ldots, I_n$ of multi-sets of events such that $\text{msgname}(e) \in \mathcal{M}$ for each $e \in \bigcup_{i=1}^{n} I_i$, we have

$$[S]^I_1(I_1, \ldots, I_n) \cap \mathcal{M} \subseteq [S']^I_2(I_1, \ldots, I_n) \cap \mathcal{M}$$

IIb for each multi-set $I$ of events such that $\text{msgname}(e) \in \mathcal{M}$ for each $e \in I$, we have $[S]^I_1(I) \cap \mathcal{M} \subseteq [S']^I_2(I) \cap \mathcal{M}$

where for a set $S$ of multi-sets of events we define $S \searrow \mathcal{M} \overset{\text{def}}{=} \{ M \setminus \{ e \in \text{Events} : \text{msgname}(e) \in \mathcal{M} \} : M \in S \}$ to be the set of multi-sets obtained by removing all events $e$ with $\text{msgname}(e) \notin \mathcal{M}$ (and extend the definition to sequences of multi-sets by applying it to the elements of the sequence). A timed (resp. untimed) $\text{MsgNm}$-refinement is simply called a (resp. untimed) refinement.

For a motivation of the refinements and comparison to other kinds, see [Jü02]. Both kinds of $\mathcal{M}$-refinement are reflexive and transitive for each $\mathcal{M}$. They preserve all $\mathcal{M}$-safety properties (i.e. sets of sequences of event multi-sets with names in $\mathcal{M}$). We show that refinement is preserved by substitution (i.e. a precongruence wrt. composition by subsystem formation).

A parameterized subsystem $S(Y_1, \ldots, Y_n)$ is a subsystem specification where $n$ of the subsystems are replaced by subsystem variables $Y_i$. For subsystems $S_1, \ldots, S_n, S(S_1, \ldots, S_n)$ is the subsystem obtained by substituting $S_i$ for $Y_i$, for each $i$, in $S$.

**Theorem 1** If $S_i'$ is a timed refinement of $S_i$ for each $i = 1, \ldots, n$ then for any parameterized subsystem $S(Y_1, \ldots, Y_n)$, $S(S_1', \ldots, S_n')$ is a timed refinement of $S(S_1, \ldots, S_n)$.

The theorem does not hold for untimed refinements [Jü02].

**Definition 2 (Behavioural Equivalence)** Two subsystem specifications $S$ and $S'$ are timed (resp. untimed) behaviourally equivalent if $\text{Msgs}(S) = \text{Msgs}(S')$, $S$ is a timed (resp. untimed) $\text{Msgs}(S)$-refinement of $S'$, and $S'$ is a timed (resp. untimed) $\text{Msgs}(S)$-refinement of $S$.

The above facts on timed refinement imply that timed behavioural equivalence is a congruence wrt. composition by subsystem formation.

Behavioural equivalence can be used e.g., to verify consistency of two subsystem specifications that are supposed to describe the same behaviour.
5. Example: Secure channel establishment

Figure 1 gives a high level system specification $S$ for communication from a sender object to a receiver object, including a class diagram with appropriate interfaces (we leave out the activity diagram which simply specifies that the two objects are executed in parallel).

Assume that an assessment of the physical layer of the system shows that security requirements are not provided. Thus we construct a refinement $S'$ in Figure 2. The class diagram needs to allow the receiver to send his certificate to the sender. The behaviour of the sender includes retrieving the public key and the certificate from the sender, checking the certificate, and encrypting the data. The receiver gives out the key and certificate first, and decrypts the received data. We assume that $\mathcal{V}er_K(\text{Sign}_K(m), m) = true$ (i.e. $\mathcal{V}er_K(m, O)$ is true if $m$ is a valid signature of $O$ with key $K$).

**Theorem 2** $S'$ is an untimed $\{\text{send, receive, return receive}\}$-refinement of $S$.

The (straightforward) proof can be found in [Jü02]. Note that the theorem does not imply that the refined protocol is secure; this has to be established by other means (including an extension of the formal semantics with adversary scenarios), see [Jü02].

Note that $S'$ is not a timed $\{\text{send, receive, return receive}\}$-refinement of $S$ (because of the delay caused by the key exchange).

6. Related Work

There has been a considerable amount of work towards a formal semantics for various parts of UML (see [Jü02] for an overview). [EFLR99]
discusses some fundamental issues concerning a formal foundation for UML. [BCR00] uses ASMs to give a semantics for statecharts. [Ste01] gives a semantics for use case diagrams based on the process algebra CCS. [ÖP00] considers interacting UML subsystems, but without giving a formal semantics. A combined formal semantics for UML statecharts and class diagrams has been given in [RCA01]. [SKM01] gives a semantics for statecharts and shows exemplarily how to check whether a set of statecharts satisfies a collaboration. Refinements have been investigated in the object-oriented setting e.g. in [DS00; DB01] where the introduced structural refinement has a similar motivation as our interface refinement. In the context of subtyping, refinement has been considered e.g. in [PH97].

7. Conclusion and Future Work

Our work shows that giving a formal foundation for complete UML specifications, rather than just single diagrams, is possible. While in our presentation here we left out some of the more advanced features of some of the considered diagrams, incorporating them is not a problem (except for an increase in complexity). This paves the way for unambiguous modelling with UML, and in particular for tool-support that can simulate specifications in their entirety. This would seem to be very useful indeed, since one of the main challenges in constructing large-scale software systems is to ensure that different components act together as expected.
ASMs proved to be a quite adequate tool to handle the complexities in formally combining the different UML diagrams, due to their flexibility. In particular, the crucial extensions over the semantics for statecharts in [BCR00] could be done in a rather natural way.

The notions of subsystem equivalence and refinement seem to be valuable tools in the construction and analysis of UML specification (although here we could only present a toy example).

On the application side, the semantics presented here has been useful in the context of security-critical systems (cf. e.g., [Jür01b]).

In further work [Jür02] we have treated other kinds of UML diagrams and other kinds of refinements (including one inspired by action refinement [GR00]). We aim to give conditions for checking equivalence and refinement on the level of syntax or by combining an object-based logic (e.g., [DKR00]) with a logic for ASMs (e.g., [PH94]).

In terms of tool-support we intend to extend the ASM-based statechart simulator presented in [CCR01] to the semantics presented here.

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References


[DKR00] Dino Distefano, Joost-Pieter Katoen, and Arend Rensink. On a temporal logic for object-based systems. In Smith and Takcott [ST00], pages 305–326.


